

Roll No.

Total No. of Questions : 09

Total No. of Pages : 02

M.Sc.(IT) (2016 to 2018) (Sem.-1)
COMPUTER ORGANIZATION AND ASSEMBLY LANGUAGE
Subject Code : MSIT-103
M.Code : 72519

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTION TO CANDIDATES :

1. SECTIONS-A, B, C & D contains TWO questions each carrying TEN marks each and students has to attempt any ONE question from each SECTION.
2. SECTION-E is COMPULSORY consisting of TEN questions carrying TWENTY marks in all.

SECTION-A

1. What are various addressing modes? Explain with suitable examples.
2. Explain the following :
 - (a) Instruction cycle
 - (b) Reverse polish notation

SECTION-B

3. Draw and explain the working of DMA controller.
4. Write notes on the following :
 - (a) Arithmetic and instruct on pipeline
 - (b) Priorities interrupt.

SECTION-C

5. Define Cache and associative memory. Discuss the need and working principle of cache coherence.
6. Write notes on the following :
 - (a) Address space and memory space.
 - (b) Significance of multiport memory

SECTION-D

7. Discuss the need and working of time shared common bus and hypercube interconnection structure.
8. What are various data transfer and arithmetic instruction in assembly language? Explain with examples.

SECTION-E

9. Write briefly :

- (a) Define Bus.
- (b) List various characteristics of RISC.
- (c) List various characteristics of CISC.
- (d) Define Trap.
- (e) What is programmed I/O?
- (f) Define DMA.
- (g) List various characteristics of multiprocessor.
- (h) List various merits and demerits of assembly language.
- (i) Define Virtual memory.
- (j) Define RAM and ROM.

NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.