

Roll No.

Total No. of Pages : 02

Total No. of Questions : 09

B.Tech. (Software Engg.) (Sem.-6)

**COMPILER DESIGN**

Subject Code : BTCS-601-18

M.Code : 92022

Date of Examination : 02-07-22

Time : 3 Hrs.

Max. Marks : 60

**INSTRUCTIONS TO CANDIDATES :**

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains FIVE questions carrying FIVE marks each and students have to attempt any FOUR questions.
3. SECTION-C contains THREE questions carrying TEN marks each and students have to attempt any TWO questions.

**SECTION-A**

1. Write briefly :

- a) Language Translator
- b) Pass Two Compiler
- c) Syntax Tree
- d) DAG
- e) Symbol Table
- f) LALR
- g) Top Down Parsing
- h) Linked Lists
- i) Regular Expression
- j) Three Address Code

## SECTION-B

2. What is compiler? Explain different phases of compiler.
3. Write a detailed note on finite automata.
4. Describe about code optimization in complete.
5. How to implement lexical analyzer? Give an example.
6. Define SLR. Write an algorithm for the construction of SLR parse table.

## SECTION-C

7. Define error. Explain in detail about lexical phase error and syntax phase error.
8. Write detailed note on:
  - a) Basic Blocks.
  - b) Flow Graphs.
  - c) Loop Unrolling.
9. What is parsing? Explain shift reducing parsing and operator precedence parsing technique.

**NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.**